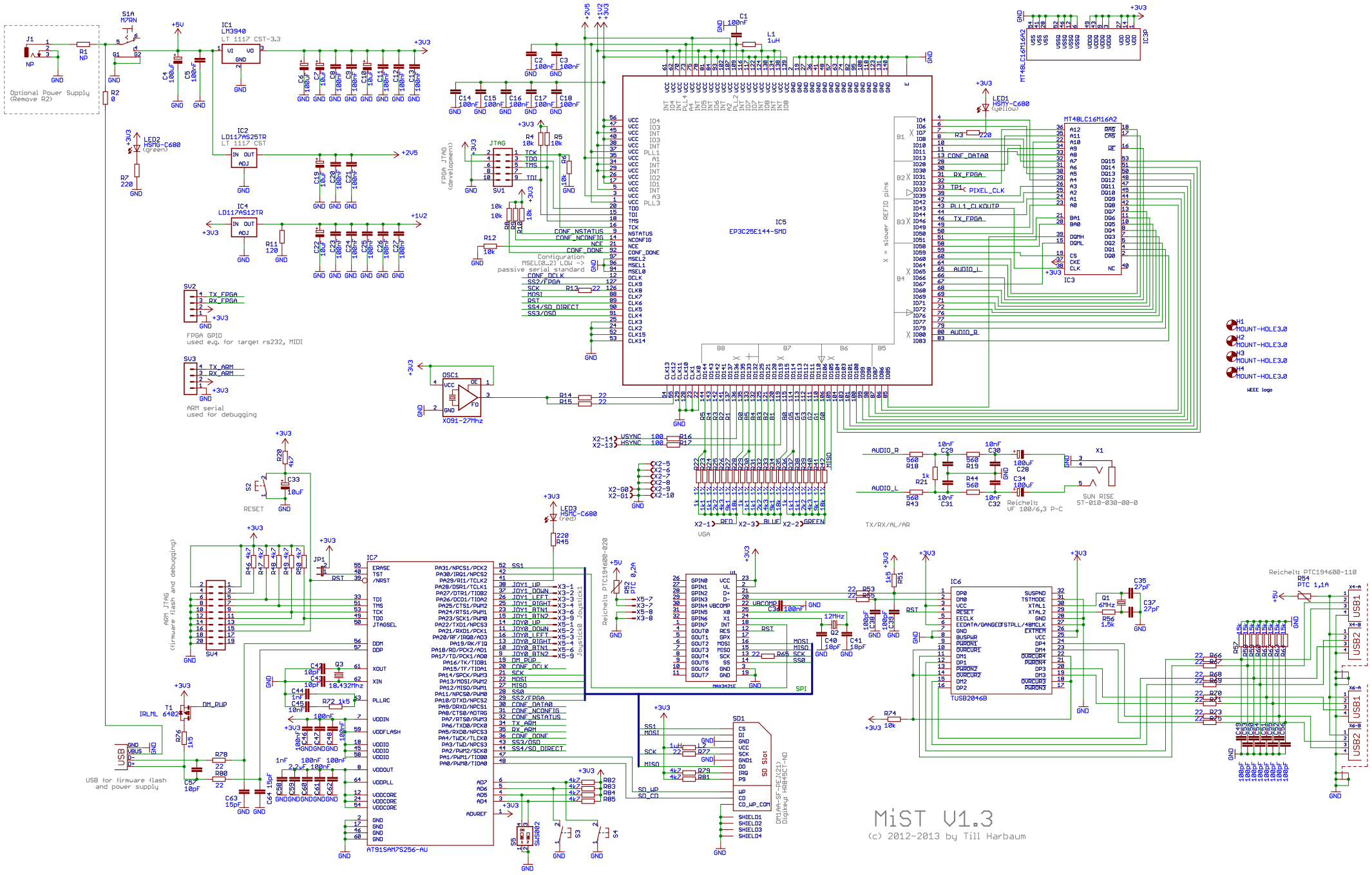
MiST V1.3
(c) 2012-2013 by Till Harbaum
Optional Power Supply (Remove R2)
FPGA JTAG (development)
FPGA GPIO used e.g. for target rs232, MIDI
ARM serial used for debugging
ARM JTAG (firmware flash and debugging)
USB for firmware flash and power supply
RESET
VGA
TX/RX/AL/AR
SPI
SD Slot
IC5 EP3C25E144-SMD
IC3 MT48LC16M16A2
IC7 AT91SAM7S256-AU
IC6 TUSB2046B
IC1 LM3940 LT 1117 CST-3.3
IC2 LD1117AS25TR LT 1117 CST
IC4 LD1117AS12TR
X = slower REFID pins
Configuration MSEL[0..2] LOW -> passive serial standard
Joystick0 Joystick1
USB1 USB2
Reichelt: PTC194600-110
Reichelt: UF 100/6,3 P-C
SUN RISE ST-010-030-00-0
MOUNT-HOLE3.0
WEEE logo